QUERY CONTROL FORM	RTIS USE ONLY	
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b. Applicant(s)	g. Disclaimer	I. Print Fig.	q. PTOL-85b
c. Continuing Data	h. Microfiche Appendix	m. Searched Column	r. Abstract
d. PCT	i. Title	n. PTO-270/328	s. Sheets/Figs
e. Domestic Priority	j. Claims Allowed	o. PTO-892	t. Other

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wherein removing a second portion of the metal oxide layer is performed in a reaction chamber in the absence of RF activation.

15. The method of claim 14, wherein heating the semiconductor substrate is at a temperature between about 625 degrees Celsius to 675 degrees Celsius.

1516. The method of claim 14, wherein the semiconductor substrate comprises silicon.

15. The method of claim 16, further comprising: forming a first interfacial oxide layer under the metal oxide layer, removing at least a portion of the first interfacial oxide after removing the portion of the metal oxide layer

1718. (Amended) The method of claim 17, wherein removing at least a portion of the first interfacial oxide layer is performed using a species containing hydrogen and fluorine.

17. The method of claim 18, further comprising forming a second interfacial oxide over the semiconductor substrate.

[721. (Amended) A method of forming a metal oxide comprising:

providing a semiconductor substrate;

forming a metal oxide layer over the semiconductor substrate; and removing a portion of the metal oxide layer by heating the semiconductor substrate and

flowing a gaseous halide;

wherein removing a portion of the metal oxide layer is performed in a reaction chamber in the absence of RF activation.

20 22. The method of claim 21, wherein the gaseous halide comprises hydrogen.

23. The method of claim 22, wherein the gaseous halide is HCl.

7.24. The method of claim 22, wherein the gaseous halide is HF.

7325. The method of claim 21, wherein the metal oxide contains hafnium and oxygen.